



DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi Vide Act 6 of 2009
Shahbad Daultapur, Bawana Road, Delhi-110042
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Academic UG Section

File No. 105(986)DTU/Acad-UG/2022-23/19330-37

Dated: -30/04/2025

OFFICE ORDER: TENTATIVE DETENTION LIST

The UG students with following Roll No. are not eligible for appearing in End Term Even Semester Examination MAY, 2025, in accordance with Ordinance & Regulation 1(B), due to shortage of attendance reports forwarded by the concerned HoDs on recommendation of Course Coordinators for the respective courses: -

UG Detention List

S.NO.	Course Code	Course Title	ROLL NO.
1	ME 102	Engineering Graphics	24/B11/58
2			24/B10/53
3	EE 102n	Data Structures Qual Algorithms	24/A15/20
4			24/A15/24
5			24/A16/15
6			24/A16/45
7			24/A16/61
8			24/B12/53
9	CO 102	Programming Fundamental	24/B12/01
10			24/B12/26
11			24/B12/43
12			24/B12/52
13			24/B12/60
14			24/B12/70
15			24/B12/74
16	CS 102	Discrete Structure	24/A05/004
17			24/A05/010
18			24/A05/016
19			24/A05/018
20			24/A05/026
21			24/A05/030
22			24/A05/045
23			24/A05/055
24			24/A05/059
25			24/A05/065
26	24/A05/071		
27	CS 104	Data Structures	24/A01/011
28			24/A01/021
29			24/A01/059
30			24/A04/027
31			24/A04/036
32			24/A04/064
33			24/A07/065


34	MC-102	Discrete Mathematics	24/B04/001
35			24/B04/048
36	MC-104	Complex Analysis	24/B04/001
37			24/B04/048
38			24/B06/060
39	AM102	Mathematics-II	24/A13/002
40			24/A13/004
41			24/A13/006
42			24/A13/008
43			24/A13/010
44			24/A13/013
45			24/A13/017
46			24/A13/024
47			24/A13/025
48			24/A13/026
49			24/A13/027
50			24/A13/028
51			24/A13/033
52			24/A13/034
53			24/A13/036
54			24/A13/039
55			24/A13/043
56			24/A13/053
57			24/A13/054
58			24/A13/055
59			24/A13/057
60			24/A13/058
61			24/B08/014
62			23/EN/019
63			24/A5/04
64			24/A5/16
65			24/A5/22
66			24/A5/24
67			24/A5/26
68			24/A5/30
69			24/A5/42
70			24/A5/43
71	24/A5/58		
72	24/A5/62		
73	24/B03/003		
74	24/B03/005		
75	24/B03/045		
76	24/B03/057		
77	24/A19/011		
78	24/A19/013		
79	24/A19/036		
80	24/A19/044		
81	24/A14/002		
82	AM102/ MA102	Mathematics-II	19/EN/053
83			21/CO/016
84			21/CO/048
85			21/CO/370

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86		21/CO/374
87		21/CO/437
88		21/CO/499
89		21/EC/140
90		21/PE/004
91		22/AE/019
92		22/CO/419
93		22/CO/509
94		22/EC/057
95		22/EC/095
96		22/EC/155
97		22/EE/144
98		22/EE/289
99		22/IT/045
100		22/IT/191
101		22/ME/028
102		22/ME/073
103		22/ME/075
104		23/BT/034
105		23/CE/059
106		23/CE/062
107		23/CE/140
108		23/CE/174
109		23/CE/175
110		23/CH/042
111		23/CH/107
112		23/CS/035
113		23/CS/192
114		23/CS/243
115		23/CS/406
116		23/CS/416
117		23/CS/540
118		23/EC/093
119		23/EC/096
120		23/EC/119
121		23/EC/178
122		23/EE/041
123		23/EE/174
124		23/EN/030
125		23/EN/038
126		23/EN/105
127		23/IT/086
128		23/IT/105
129		23/MC/137
130		23/MC/143
131		23/MC/191
132		23/MC/202
133		23/ME/079
134		23/ME/115
135		23/ME/141
136		23/ME/178
137		23/PE/088

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138			23/SE/121
139	EC 208	Computer Architecture	23/EC/95
140			23/EC/96
141			23/EC/103
142			23/EC/104
143	EE202a/EE210n	Linear Integrated Circuits	23/EE/304
144			23/EE/310
145			23/EE/313
146			23/EE/317
147		23/EE/332	
148	EE282a/CH202n	Instrumentation & Process Control	23/CH/87
149			23/CH/97
150			23/CH/100
151			23/CH/103
152		23/EE/111	
153	EE 206	Control System	23/EE/304
154			23/EE/313
155			23/EE/317
156			23/EE/332
157	EE 204	Power Transmission and Distribution	23/EE/304
158			23/EE/317
159			23/EE/332
160	CS 204	Theory of Computation	23/CS/05
161	CS 206	Computer System Architecture and Organization	23/CS/05
162			23/CS/25
163			23/CS/30
164			23/CS/04
165			23/CS/28
166			23/CS/61
167			23/CS/74
168			23/CS/81
169			23/CS/89
170			23/CS/92
171			23/CS/93
172			23/CS/97
173			23/CS/99
174			23/CS/101
175			23/CS/105
176			23/CS/115
177			23/CS/119
178	23/CS/121		
179	23/CS/127		
180	23/CS/133		
181	23/CS/137		
182	23/CS/509		
183	23/CS/526		
184	23/CS/527		
185	2k19/CO/327		
186	CS 208	Computer Network	23/CS/243
187			23/CS/258
188			23/CS/288
189			23/CS/289



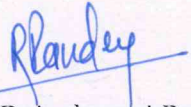
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191			23/CS/025
192			23/CS/030
193			23/CS/035
194	CS 210	Database Management Systems	23/CS/061
195			23/CS/070
196			23/CS/405
197			23/CS/438
198			23/CS/398
199			23/CS/415
200	EC 302	VLSI Design	2k22/EC/129
201	EC 304	Digital Signal Processing	2k22/EC/129
202			2k21/CO/540
203	EE 308	Power System Operation & Control	2k21/EE/157
204			2k22/EC/129
205			2k22/CO/258
206			2k22/CO/299
207			2k22/CO/383
208			2k22/CO/384
209			2k22/CO/419
210	CO 302	Compiler Design	2k22/CO/428
211			2k22/CO/450
212			2k22/CO/531
213			2k22/CO/111
214			2k22/CO/115
215			2k22/CO/101
216			2k22/CO/104
217	EE 306	Microprocessor and Microcontroller Application	2k22/EE/144
218			2k22/EE/161
219	CO 304	Artificial Intelligence	2k22/CO/506
220			2k22/CO/222
221	CO 328	Deep Learning	2k22/EE/161
222			2K22/MC/67
223			2K22/MC/72
224			2K22/MC/117
225			2K22/MC/120
226			2K22/MC/122
227			22/MC/129
228			22/MC/130
229			22/MC/131
230			22/MC/132
231	MC-306	Financial Engineering	22/MC/133
232			22/MC/136
233			22/MC/137
234			22/MC/139
235			22/MC/140
236			22/MC/142
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264			21/MC/147
265	IMSMA-104	Real Analysis	2K24/IMSMA/01
266			2K24/IMSMA/12
267			24/BBA/130
268	BBA 104	Business Statistics & Operations Research	24/BBA/146
269			24/BBA/156
270			24/BBA/175
271			24/BBA/07
272			24/BBA/10
273			24/BBA/19
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280			24/BBA/46
281			24/BBA/47
282	ECO 102	Principle of Macroeconomics	24/BBA/51
283			24/BBA/58
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302	BAE 106	Mathematical Methods for Economics	24/BAE/19		
303			24/BAE/27		
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311	BBA 216	Consumer Behaviour	23/BBA/42		
312			23/BBA/47		
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327			23/BBA/48		
328			23/BBA/51		
329			23/BBA/57		
330			23/BBA/69		
331	BBA-208	Written Analysis of Cases	23/BBA/78		
332			23/BBA/93		
333			23/BBA/102		
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348			23/BAE/20
349			23/BAE/21
350			23/BAE/41
351			23/BAE/42
352			23/BAE/47
353	BAE210	Logical Reasoning	23/BAE/77
354			23/BAE/89
355			23/BAE/118
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357			23/BAE/147
358			23/BAE/166

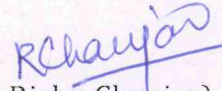
This is issued with the approval of the Competent Authority.


 (Prof. Rajeshwari Pandey)
 Dean Academic (UG)
 Dated: - 30/04/2025

File No. 105(986)DTU/Acad-UG/2022-23 /19330-37

Copy for information and further necessary action: -

1. PA to the Hon'ble Vice-Chancellor, DTU.
2. Registrar, DTU.
3. Controller of Examinations.
4. All HoDs:- With the request to display on student notice board.
5. Chief Operating Officer.
6. Head (CC) : With the request to display on DTU website.
7. Guard File.


 (Prof. Rishu Chaujar)
 Associate Dean Academic (UG)