

No. F. DTU/SP/211/12-25/15-16

Dated:

### NOTICE INVITING TENDER

E- Tenders along with illustrated literature/leaflets for the supply/execution of item(s)/stores/work detailed below are invited from the Manufacturers or their Authorized Distributors only in two-bids system **(the bidder should be registered with the Delhi VAT Department and carry a valid Tax Identification Number issued by it to ensure that the delivery of goods is made from Delhi against a sale invoice issued from Delhi only)** through 'e' procurement solution only as per the guidelines and terms & conditions given in tender document - details of the NIT along with terms & conditions, specifications etc. can be seen/downloaded at/from the website.

The interested tenderers should upload duly signed tender form and their bids along with scanned copies of all the relevant certificates, documents etc. in support of their technical & price bids - all duly signed - on the: <https://govtprocurement.delhi.gov.in>. latest by **28/07/2016 up to 2.30 P.M.** An index prepared after pagination of all documents may also be uploaded. The technical bids will be opened online on **28/07/2016 at 3.00 P.M** {those bidders only whose original instrument of EMD amount is dropped in Tender Box placed in the office of Officer In-charge (S&P)} in the presence of the bidders who wish to be present and will also be displayed on the website. For participation in the tender through e-procurement solution, the tenderers are required to have digital certificate and get registered with application Service Provider NIC.

Tender document is also available for viewing on the website of Delhi Technological University, Delhi at [www.dtu.ac.in](http://www.dtu.ac.in)

Yours faithfully,

**EMD: Rs. 6,100/-**

Officer In-charge (S&P)

Supply following items		
S.No.	Description of item	Quantity
1	ARM9 LPC29xx Development Board	02 sets
2	Signal Conditioning Board for Voltage & Current Measurement to DSP/FPGA/MCU	02 sets
3	Spartan 3AN FPGA KIT	02 sets
4	SPP 1.04 Programmer	03 nos.
5.	Gesture Recognition Kit	01 set
6.	MSP430 Development board	04 set
7.	FPGA KIT Spartan- 6 Based	01 set
8.	RASPBERRY Pi B+	02 sets
9.	TMS320F2812 Stick Board	02 nos.
10.	Embedded Dev Board for Atmel, NXP or Equivalent Flash 89CXX MCUs.	04 nos.
11.	DualPIC Stand Alone Programmer	01 no.
12	MPLAB ICD3 In- Circuit Debugger/Programmer	01 no.
13	Dev Board for 16-bit PIC24 MCUs, 16 bit dsPIC33 DSCs & 32-bit PIC32MX MCUs	02 nos.
14.	Demo Board for 8-bit PIC 16/PIC18	02 nos.
15	44 PIN Demo Board (PIC 18 F45K20)	04 nos.
16.	44PIN Demo Board (PIC 16F887)	04 nos.

## Detailed Technical Specification as under:

### 1. ARM9 based Development Board

ARM9 based Microcontroller board encouraging the advance technology like CAN, LIN and USB OTG. Should also support the Real Time Applications (RTOS). More special features are SPI, UART, I2C, ADC, PWM, TIMER, Quadrature Encoder, Watchdog Timer etc., Flexible Clock and Reset Generator Unit (CGU & RGU).

#### Features

- CAN Ver. 2.0 Communication
- 128x64 Graphical LCD
- 2X16 Character LCD (Background Light)
- Serial EEPROM (I2C Devices)
- Interrupts Study, Reset Button
- 2 No's of UART(RS232)
- DS1307 RTC with Batter-Backup
- 2 No's of Analog Input (Potentiometer) for ADC
- Temperature Sensor(LM35)
- 8 No's of Point LEDs (Logic Output)

### 2. Signal Conditioning Board for Voltage & Current Measurement to DSP/FPGA/MCU

#### Measurement Rating

##### *Voltage rating:*

- Input voltage - 0-230 VAC
- Output voltage - 0-3 VAC
- Quantity of the unit - 3 NOS

##### *Current rating:*

- Input current - 0-20 AMP AC/DC
- Output current in terms of voltage - 0-3 VOLTS AC/DC
- Quantity of the unit - 3 NOS

##### *Unit operating power:*

- Voltage - +15, -15, +5
- Current - 1A

##### *Transformer Details*

- For unit operation - (0-230) / (12-0-12) VAC/1A - 1Nos
- For Reference voltage of three phase - (0-230) / (0-9) VAC/700mA - 3Nos

### **Current Transducer**

#### **Electrical data:**

- Normal current IPN : 25A rms
- Measuring range IP : 0~±80A
- Secondary nominal output : 2.5V ± 0.625V
- Ratio : 1 : 2000
- Load resistance RL : ≥ 2k Ω
- Supply voltage : +5(1 ±5%)V
- Current consumption (&commat;+5V) : ≤30 mA
- Isolation: Between primary and secondary: 3kV /50Hz/1min

#### **Accuracy - Dynamic performance data:**

- Accuracy : TA=+25°C ±0.7%
- Non-linearity : ≤ 0.1%
- Offset of zero current : 2.5V ± 0.025V (+25 ° C)
- Thermal drift: -10° C~ +85° C : ≤ 100ppm/ °C  
: @-40° C~-10° C : ≤ 150ppm/ °C
- Response time : ≤400ns
- di/dt accurately followed : ≥15A/us

#### **General data:**

- Operating temperature : -40 ° C~+85° C
- Storage temperature : -40 ° C ~ +100 ° C
- Weight : ≤11g
- Standards : EN50178:1997

### **3. FPGA Kit, Spartan-3 based**

#### **Key Features**

- \* 2-Channel ADC(On-Board)
- \* Single Channel DAC
- \* 2x16-Char LCD Interface
- \* 8 No's of Point LEDs (Logic Output)
- \* 8 No's of Slide Switches(Digital Input)
- \* 5 No's of pushbutton Interface
- \* One Reset switch
- \* 2 No's of UART
- \* Buzzer
- \* 50 MHz crystal oscillator clock source

- \* 40-pin I/O connector for interface external peripheral modules
- \* On-board 5V, 3.3V and 1.2V regulators.

#### 4. Serial Port Programmer for 8051 Family:

Serial Port Programmer for 40pin & 20pinFlash 89CXX series of MCUs. Should contain –

- 40pin ZIF Socket & 20pin ZIF Socket.
- Built-In Linear Power Supply
- RS232 Serial Cable
- CD containing Setup & Datasheets.

Features –

- Windows based GUI with MCU database in MS Access.
- Programming Drivers with correct timing algorithms.
- Separate windows for different memory region.
- Special Commands for Programming Boot Vector, Boot Loader, User ID, 6X/12X, etc.

#### 5. Gesture recognition Kit

##### Key Features

- MGC3130 3D Tracking and Gesture Controller
- Built-In 7" frame electrodes
- Interface select
- LED bar signalling when board is powered and indicating the communication status
- Microchip's PIC18F14K50 USB microcontroller passing messages between MGC3130 and the PC
- USB mini-B connector to connect the board to a PC
- Reset button resetting the MGC3130
- Microchip's MCP1801 LDO voltage regulator converting 5V USB power to 3.3V board supply
- Acrylic glass (180 x 116.5 x 2 mm) simulating the housing of a target device
- Plastic rivets mounting the acrylic glass to the PCB
- Supports Windows XP, Windows 7 or Windows 8 Operating system.

##### Applications

- Position tracking
- Gesture recognition
- Demo applications

##### Core Features

- Hand Position Tracking in three dimensions (x, y, z)
- Hand Gesture Recognition based on a stochastic Hidden Markov Model (HMM)
- Approach detection for power saving
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## **6. MSP430 Development Board specification**

- 8 LEDs (Logic Output)
- 8 Digital Input (Slide Switch)
- 4x4 Matrix Keypad
- 2X16 Character LCD (Background Light)
- 4 Nos. 7-Segment Display Using I2C
- 2 Nos. Analog Input (Potentiometer)
- Temperature Sensor(LM35)
- Stepper Motor Interface
- 2 SPDT Relay
- PS2 Keyboard
- I2C RTC With Battery Backup
- UART(RS232)
- USB 2.0 Device | Virtual Port
- Buzzer
- PWM Output
- Interrupts Study, Reset Button
- 4KB Serial EEPROM (I2C Devices)
- 20-Pin Expansion Connector
- Traffic Light Modules
- 128x64 Graphical LCD.

## **7. FPGA klt Spartan -6 based**

- 16 Nos. DIP Switch (Digital Input)
- 16 Nos. Point LEDs (Logic Output)
- 2 Nos. of Push Buttons
- Two UART(RS232)
- 12-Bit SPI ADC (2 channel)
- 12-Bit SPI DAC
- Temperature Sensor LM35
- 5V SPDT Relay
- Buzzer (Alarm)
- Reset Button | Power-on Indication
- JTAG (Program/Debug)
- 40 Pin and 20 Pin I/O Expansion Connector
- On-Board Voltage regulators +5V | +3V3 | +1V2

- 128x64 GLCD Module

### **8. RASPBERRY PI B+ KIT**

Chip: Broadcom BCM2835 SoC or equivalent

Core architecture: ARM11

CPU: 700 MHz Low Power ARM1176JZFS Applications Processor

Provides Open GL ES 2.0, hardware-accelerated OpenVG, and 1080p30 H.264 high-profile decode

Capable of 1Gpixel/s, 1.5Gtexel/s or 24GFLOPs with texture filtering and DMA infrastructure

Memory: 512MB SDRAM

Operating System: Boots from Micro SD card, running a version of the Linux operating system

Dimensions: 85 x 56 x 17mm

Power: Micro USB socket 5V, 2A

#### **Connectors**

Ethernet: 10/100 BaseT Ethernet socket

Video Output: HDMI (rev 1.3 & 1.4)

Composite RCA (PAL and NTSC)

Audio Output: 3.5mm jack, HDMI

USB: 4 x USB 2.0 Connector

GPIO Connector: 40-pin 2.54 mm (100 mil) expansion header: 2x20 strip

Providing 27 GPIO pins as well as +3.3 V, +5 V and GND supply lines

Camera Connector: 15-pin MIPI Camera Serial Interface (CSI-2)

JTAG: Not populated

Display Connector: Display Serial Interface (DSI) 15 way flat flex cable connector with two data lanes and a clock lane

Memory Card Slot: SDIO

## **9. DSP Board (TMS320F2812 Stick Board)**

### **Key Features**

- On Board XDS100 USB Emulator.
- Flash Memory Up to 128K x 16 Flash.
- ROM Memory Up to 128K x 16 ROM.
- 16 x 16 and 32 x 32 MAC Operations.
- 150 MHz (6.67-ns Cycle Time).
- 1K x 16 Each SARAM, 25MHz on board crystal.
- Motor Control Peripherals.
- 12-Bit ADC, 16 Channels.
- On board USB-UART with line driver.
- SCIA or SCIB can configure single USB-UART.
- 2 Digital LED Output, 1 Digital Input (Push Switch).
- 8 Analog Inputs.
- Multiple Booting Option Connectors.
- Onboard IEEE 1149.1 JTAG emulation connector.

### **TEXAS - TMS320F2812**

- Flash Devices: Up to 128K x 16 Flash (Four 8K x 16 and Six 16K x 16 Sectors)
- ROM Devices: Up to 128K x 16 ROM
- 1K x 16 OTP ROM
- L0 and L1: 2 Blocks of 4K x 16 Each Single-Access RAM (SARAM)
- H0: 1 Block of 8K x 16 SARAM
- M0 and M1: 2 Blocks of 1K x 16 Each SARAM

#### **10. Embedded Dev Board for Atmel, NXP or equivalent Flash 89CXX MCUs.**

Should Contain –

- Onboard 40pin DIP NXP P89C60X2 or equiv. & 20pin DIP Atmel 89c2051 or equiv.
- 9V Power Adapter (LP9VPS-AC162039-2).
- RS232 Serial Cable.
- 20pin Flat Ribbon Cables.
- CD containing freeware IDEs, Assemblers, Simulators & Sample Programs.

Features –

- Interfaces: Buffered I/O, Standard 8keys KeyPad, 4x3 Matrix Keypad, RS232 Interface, SPI & I2C EEPROMs & RTC (with Li Battery), PC-AT Keyboard Jack, 16x2 LCD, 8 LEDs, 7 Segment Displays, ADC/DAC, Relays, etc.

#### **11. DualPIC Stand Alone Programmer.**

Should contain –

- One DualPIC Programmer.
- One A to mini-B USB cable.
- One USB Adaptor.
- Two 6-pin ICSP Male to 6pin ICSP Female.
- CD containing MPLAB IDE, Demo version C Compilers from Microchip, CCS, etc. Webinars on MPLAB Tools, Manuals & User Guides.

Features → Internal Two Independent Programmers each with its own EEPROM for storing Hex Image and each with its own LED status

- Two different target devices can be programmed independently.
- One single 'PROGRAM' switch to simultaneously program the devices.

#### **12. MPLAB ICD3 In-Circuit Debugger/Programmer.**

Should contain → USB cable, RJ11 Cable.

- CD containing MPLAB IDE, Demo version C Compilers from Microchip, CCS, Hi-Tech, IAR, etc., Webinars, Manuals & User Guides.
- **Self Test Interface Module.** with High & Full USB Speed.
- High Speed Programming up to 15x over ICD2.
- Internal FPGA Hardware Accelerator.
- Internal SRAM buffers (1Mx8) to download Program Memory Image for faster programming.
- Programmable Vpp & Programmable Vdd.
- Hardware OV/OI Protection
- Complex Breakpoints, 999 Software Breakpoints.
- A Production Programmer.



**13. Dev Board for 16-bit PIC24 MCUs, 16-bit dsPIC33 DSCs & 32-bit PIC32MX MCUs.** containing –

- Sample 100pin TQFP PIC24FJ128GA010 & 100pin TQFP dsPIC33FJ256GP710 PIMs.
  - RS232 Serial Cable.
  - CD containing MPLAB IDE, Demo programs, Graphics Library, Manuals & User Guides.
- Features → ICD2/ICD3 Interface. • RS232 Interface. • USB Interface. • 16x2 LCD, 8 LEDs, 4 Switches. • Temp. Sensor, Potentiometer. • PIC tail Plus Daughter Board Expansion Connector. with required power supply adaptor.

**14. Demo Board for 8-bit PIC16/PIC18.**Containing

- Sample 40pin DIP PIC16F877 & 40pin DIP PIC18F452 MCUs.
  - RS232 Serial Cable.
  - CD containing MPLAB IDE, Demo programs, Manuals & User Guides.
- Features → ICD2/ICD3 Interface. • RS232 Interface. • 16x2 LCD, 4 LEDs, 3 Switches. • Piezo Buzzer, Temp. Sensor, Potentiometer with required power supply adaptor.

**15. Demo Board (PIC18F45K20)**

A small demo board with a PIC18F45K20 device on board and a small, surface-mount prototype area.

**16. 44PIN Demo Board (PIC16F887)** a small and simple demonstration PCB for 44 pin Thin quad flatpack (TQFP) PIC microcontroller Units.

**Note: All kits should have supporting software in cd, pc connecting cables and in-built or external power supply adaptor as a part of package.**

(TO BE SUBMITTED ALONG WITH TECHNICAL BID)

**TENDER NOTIFICATION NO:** -----

Phone No:- 27871018

UNDERTAKING

The Registrar,  
Delhi Technological University,  
Bawana Road,  
Delhi-110042

We the undersigned (herein after called as Contractor/Vendors/Suppliers) hereby offer to execute supply of items as per specification against which we have quoted over rates and for which this tender may be accepted at the rates stated there in and subject to the terms & conditions set forth for such items as may be ordered by the Registrar, Delhi Technological University or officer acting on his behalf.

**Date this** \_\_\_\_\_ **Day of** \_\_\_\_\_

**Signature of Contractor** \_\_\_\_\_

**Address** \_\_\_\_\_

\_\_\_\_\_

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## Guidelines/Procedure to be followed in introduction of 'e'-procurement solution:

**1. Payment of cost of Tender documents:** The collection of cost of Tender documents is dispensed away with, as there is no physical supply of tender documents and also to have absolute anonymity of bidder participating in e-procurement solution. The bidders can view/download the tender documents from the: <https://govtprocurement.delhi.gov.in>.

**2. Submission of bids:** The bidders who are desirous of participating in 'e'- procurement shall submit their price bids in the standard formats prescribed in the Tender documents, displayed at : <https://govtprocurement.delhi.gov.in>. The bidder should upload the scanned copies of all the relevant certificates, documents etc. in the: <https://govtprocurement.delhi.gov.in>. in support of their price bids. The bidder shall sign on all the statements, documents, certificates, uploaded by him, owning responsibility for their correctness/authenticity and copies thereof may also be submitted in the office of the Officer In-charge (S&P), DTU along with original EMD before the specified date & time. In the event of the specified date for physical submission of EMD along with copies of bid documents is declared a holiday, the same will be received up to the appointed time on the very next working day. However, documents of the bidders downloaded online or requisitioned subsequently only will form the basis for deciding the tender.

**3. Payment of Bid Security (Earnest Money Deposit):** The EMD shall be in the form of the **DD/BG/Fixed Deposit Receipt only** of a scheduled bank issued in favour of Registrar, Delhi Technological University, Delhi and the zerox copy thereof is to be scanned and uploaded along with the bid, and the original shall be sent to DTU so as to reach before the date & time of closing of the bids; failing which bid will be rejected. The Bid Security (EMD) of unsuccessful bidders will be discharged immediately after finalization of the order/contract without any interest. However, firms registered with NSIC etc., are exempted from submitting EMD provided such registration includes the item(s) they are offering are manufactured by them and not for selling products manufactured by other companies.

**4. Price Bid Opening:** The Price Bids of only technically qualified bidders (whose bids satisfy the prescribed technical specifications/parameters and have also submitted all requisitioned documents & EMD) will be opened online at the specified date & time and will subsequently be evaluated to determine the lowest bidder. The result will be displayed on the: <https://govtprocurement.delhi.gov.in>. which can be seen by all the bidders who participated in the tenders. There shall not be any negotiation normally. However, in exceptional cases, negotiations can be held with the lowest evaluated responsive bidder only. Counter offers tantamount to negotiations and shall be treated at par with negotiations.

**5. Processing of Tenders:** The concerned officer/officers will evaluate and process the tenders as done in the conventional tenders and will communicate the decision to the bidder online.

**6. Payment of Performance Security:** The successful tenderer shall furnish a Bank Guarantee/FDR of the value of 05% of the basic cost of the item for a period of 14 months beyond the warranty period from a nationalized bank to ensure the satisfactory performance of item supplied. The performance guarantee is to be submitted at the time of installation / demonstration of equipments. In case the performance of the item is not found satisfactory, the amount of Performance Security will be forfeited & credited in university account.

**7. Participation of Bidders at the time of opening of bids:** Bidders have two options to participate in tendering process at the time of opening of Bids:

- (i). Bidders can come at the place of opening of bids (electronically) as done in the conventional tender process.
- (ii). Bidders can visualize the process online.

**8. Participation Financial Rules for e-procurement:** The e-procurement system would be applicable for purchase of goods, outsourcing of services and execution of work as prescribed in GFRs.

OFFICER IN-CHARGE (S&P)  
DELHI TECHNOLOGICAL UNIVERSITY,  
SHAHBAD DAULATPUR, BAWANA ROAD, DELHI - 110 042

## TERMS AND CONDITIONS

Procedure for submission of bids: The bidders who are desirous of participating in 'e'- procurement shall submit their technical and price bids in the standard formats prescribed in the Tender documents, displayed at: <https://govtprocurement.delhi.gov.in>. The bidder should upload the scanned copies of all the relevant certificates, documents etc. after page-numbering all documents and tender document and prepare an index thereof in the: <https://govtprocurement.delhi.gov.in>. in support of their price bids. The bidder shall sign on all the statements, documents, certificates, uploaded by him, owning responsibility for their correctness/authenticity and copies thereof may also be submitted in the office of the Officer In-charge (S&P), DTU along with original EMD. However, documents of the bidders downloaded online or requisitioned subsequently only will form the basis for deciding the tender.

1. Manufacturers (OEMs) or their Authorized Distributors/Suppliers / Agents/Channel Partners only should submit their bids.
2. ALTERATION IN THE SPECIFICATION.
  - (i) The specifications mentioned/issued with this form of tender must not be altered by the Suppliers.
3. INCOMPLETE TENDERS
4. The Bidder is expected to examine all instructions, forms, terms and specifications in the bidding documents. Failure to furnish all information/documents, as asked for in the NIT, or submission of a bid not substantially responsive to the NIT in every respect, will be at the Bidder's risk and may result in rejection of its bid.
5. CANCELLATION OF TENDER/ CONTRACT/ IN PART OR IN FULL IN CASE OF DEFAULT IN CONTRACT/SUPPLY:

If the Supplier, in the opinion of the Institute, fails or neglects to comply with any of the terms & conditions forming, part of the order issued, the head of institute shall without prejudice to any other right or remedies under the contract, has the right to cancel the contract / order by giving 15 days notice in writing to the Suppliers/firms without being liable to pay compensation for such cancellation.
6. Tender shall be uploaded as per guidelines indicated for e-procurement solution.
7. Demonstration of equipments has to be arranged by the suppliers, if desired by the institute. The technical committee may visit production facility if so desired for sample verification.
8. **The quotation should be valid for a period of one year from the date of opening of the tender.**
9. Rates are to be quoted in INR (Rupee terms) only and any revision thereof is not allowed after the tenders have been opened.
10. The delivery period should be clearly mentioned against each item, incase, the items are not readily available, ex-stock offer will be preferred.
11. Rates should be quoted F.O.R Institution. Taxes and Duties namely Sales tax/VAT/Custom Duty (against Custom Duty Exemption Certificate)/Excise Duty should be mentioned clearly.
12. Consignment will not be insured at the Institute/University Cost.
13. **Bidder, if is not the Original Equipment Manufacturer (OEM), must submit OEM's or their Distributor's Authorization to quote/sell the product(s).** Preference will be given to quotation pertaining to indigenous products. However, where suitable substitutes are not available and item need to be imported the following clarification/information should be given.
  - o Whether the item will be imported by the intended tenderers against its own import license or university will have to provide Custom Duty Exemption Certificate (CDEC).
  - o Name and address of the foreign supplier, make & model of the offered product and authorization to sell from OEM or their Distributor/ Authorized Chanel Partner.
  - o Delivery period including information about mode of dispatch and possible duration (after dispatch) for receipt of item at the port.
  - o Whether the item required any special preparation for installation. In case yes, full details should be given regarding operation maintenance of the items.
  - o In case of costly/sophisticated items whether the tenderers will arrange any special training regarding operation / maintenance of the items.
  - o Nature of assurance for the supply of spares after the warranty period.
14. The payment will be made within 30 days after the successful demonstration/installation of the equipment and fulfilling of other obligations (like training etc., if any) as per the purchase/work order, against a Bill/Invoice; containing therein details of goods delivered/services performed. Rejected items/goods should also be removed within 30 days after which no responsibility will be accepted by University.

15. In the event of the item(s) being imported product(s), Custom Duty Exemption Certificate (CDEC) will be issued by the University on the written request of the supplier; who, in turn, will furnish copies of relevant Customs Related Documents namely Airways Bill, Packing List, TR-6 challan etc. along with Bill/Invoice.
16. Conditional quotations and/or incomplete quotations in any respect will be rejected.
17. In case you cannot quote for one or more of the items asked for in the tender the word "NOT QUOTED" (in the rate column) should be indicated.
18. The specification of the item quoted by the firm should confirm to the University specifications. Confirmation, in this respect should be specifically mentioned in the tender. Where the tenderer feels that the specification of the item not fully given or differ, from the specification of the item mentioned by the university, the exact specification of such item should be attached with the tender indicating the item quoted.
19. The Firm is required to link the University specifications with catalogues & leaflets/literature and also **mention Make and the Model for each item**. Detailed features, for compliance of specification should be provided on specification sheet & appropriate reference i.e. page no. & para of literature, leaflet wherefrom the relevant information has been checked, should be indicated.
20. **EARNEST MONEY:-** EMD should be attached with the technical bid. The EMD shall be in the form of the **DD/BG/Fixed Deposit Receipt** of a scheduled bank issued in favour of Registrar, Delhi Technological University, Delhi. Zerox copy thereof is to be scanned and uploaded along with the bid, and the original instruments shall be sent to DTU so as to reach before the date of closing of the bids. Failure to furnish the original instrument before the closing of the bid, will entail rejection of bid and blacklisting. **If the tenderer after acceptance of the tender refused to take up the purchase order, his Earnest Money will be forfeited.** Any tender received without / less Earnest Money deposit shall be summarily rejected.
21. The Purchase Order/Contract will be awarded to the successful Bidder whose bid has been determined to be responsive and has been determined to be the lowest evaluated bid, provided further the Bidder is determined to be qualified to execute the Order/Contract satisfactorily.
22. There shall not be any negotiation normally. However, in exceptional cases, negotiations can be held with the lowest evaluated responsive bidder only. Counter offers tantamount to negotiations and shall be treated at par with negotiations.
23. The Competent Authority reserves the right to reject any or all the tenders and annual the bidding process at any time prior to award of Contract, without assigning any reason, without thereby incurring any liability to the affected Bidder or Bidders, and his decision will be final.
24. The supplies shall have to be made within the period specified in the purchase order failing which the order shall be cancelled and the Earnest Money will be forfeited. However, in exceptional circumstance and, on written request, from the supplier/tenderer, extension of date for supply of the material will be considered. Extension in supply period is at the sole discretion of the competent authority.
25. Service manuals, wherever available/ required, should be provided along-with the Equipments.
26. The University reserves the right at the time of award of PO/Contract to increase or decrease the quantity of goods and services originally specified in the Schedule of Requirements without any change in unit price or other terms and conditions. Further, the quantities in the PO/Contract may be enhanced by 30% within the delivery period.
27. In the case of purchase of may items against one tender, which are not inter-dependent or where compatibility is not a consideration, comparison would be made on the basis of prices quoted by the firms for identifying the lowest quoting for each item.
28. **WARRANTY: All products must have a minimum of One Year Warranty.** A Warranty Certificate should invariably be supplied along with the item at the time of delivery. If after delivery, acceptance and installation and within the guarantee and warranty period, the operation or use of the goods proves to be unsatisfactory, the supplier shall rectify the defects, errors or omissions by repair or by partial or complete replacement on free of cost basis.
29. The Competent Authority reserves the right to levy liquidated damages up to 2% of the value of the order for delayed supply. If the supply is delayed beyond the extended period, the University reserves the right even to cancel the order and forfeit the EMD of the firm/ tenderer.
30. **PERFORMANCE SECURITY DEPOSIT:-** The successful tenderer shall furnish Performance Security Deposit of the value of 05% of the basic cost of the item in the shape of Bank Guarantee/FDR etc. from a nationalized bank pledged to Registrar, DTU, for a period of 14 months beyond the warrantee period to ensure the satisfactory performance of item supplied. The performance guarantee is to be submitted at the time of installation / demonstration of equipments. In case the performance of the item is not found satisfactory and/or the Supplier fails to complete its obligation under the contract/purchase order, the amount of Performance Security will be credited in University account.
31. **DEFAULT: -** In the event of default and unsatisfactory service of the contractor/Supplier firm, the DTU will be at liberty to repair/get the item serviced from other party at the cost of supplier/ contractor/ tenderer.
32. In case of software items, the suppliers should ensure that:-
  - o Legal software is supplied in original sealed pouches / P. K. T.

- A license agreement is enclosed with it.
  - A registration card is available for software.
33. FAILURE AND TERMINATION: - If the Contractor / Supplier fails to deliver the stores or any installment thereof within the period fixed for such delivery or at any time repudiates the contract before the expiry of such period, DTU may without prejudice to the right of the purchaser recover damages for breach of the contract.
  34. The technical & financial bids of only those bidders will be opened who fulfill the eligibility criteria and whose documents are found in order. If any of the date earmarked for opening of technical or financial bids happens to be a holiday, the bids will be opened on the very next working day.
  35. Notwithstanding the provisions relating to extension of time, penalty and cancellation of tender/contract for default, the vendor shall not be liable for forfeiture of its performance security/ liquidated damages or termination for default, if and to the extent that, its delay in performance or other failure to perform its obligations under the contract is the result of an event of Force Majeure (i.e. an event or situation beyond the control of the vendor that is not foreseeable, is unavoidable, and its origin is not due to negligence or lack of care on the part of the vendor; such as wars or revolutions, fires, floods, epidemics, quarantine restrictions and freight embargoes etc.). If the performance in whole or in part or any obligation under the contract is prevented or delayed by any reason of force-majeure for a period exceeding 60 days, either party may at its option terminate the contract by sending a written notice without any financial repercussions on either side.
  36. For any query/clarification in r/o technical aspect of the enquiry, **HOD (Electrical Engg. Department)** , DTU may be contacted.
  37. EMD in the form of DD/BG/FDR only No. with date is enclosed.
  38. Prof of VAT/Sale Tax Registration no. and VAT Return copy of fourth quarter for last three financial year date of tender.
  39. PAN No (Copy PAN no. Attached )
  40. P.Os Copies /rate having executed similar items/order copy with at least 03 financial year date of tender.
  41. Authorization certificate from Original equipment Manufacturer (OEM) or their Distributor to quote/sell the product, in case the bidder is not the OEM.
  42. Disputes, if any, arising out of this tender shall be subject to exclusive jurisdiction of Courts of Delhi/New Delhi only.

OFFICER IN-CHARGE (S&P)  
DELHI TECHNOLOGICAL UNIVERSITY,  
SHAHBAD DAULATPUR, BAWANA ROAD, DELHI - 110 042

CHECK LIST OF DOCUMENTS TO BE SENT WITH TECHNICAL BID.

S.No.	Particulars of documents	Page no.	No. of pages
1.	Proof of EMD (mention amount with instrument number and date)		
2.	Proof of PAN no. (mention no.....)		
3.	Proof of VAT/Sales Tax Registration No and VAT Return copy of fourth quarter ( January to March ) for last three financial year date of tender.		
4.	Brochure/Leaflets/Technical Information, including Make & Model, Imported/Indian of the item(s)		
5.	UNDERTAKING as per page no. 10 of Tender Document, duly signed.		
6.	Technical specifications, terms & conditions and delivery period etc. to be submitted on firm's letter head		
7.	Warranty Certificate from manufacturer or authorized dealer of manufacturer		
8.	P.Os Copies having executed similar items/order copy only With at least 3 financial years date of tender.( minimum one purchase order copy Per financial year )		
9.	Authorization Certificate from Original Equipment Manufacturer (OEM) or their Distributor to quote/sell the product, in case the Bidder is not the OEM		

Note: All copies of above documents should be duly signed and stamped by the tenderer before uploading.

Signature of tenderer: .....

Name: .....

Name of firm: .....

Telephone No.....

**INDEX**

S.No.	Particulars of documents	No. of pages

**Pagination must be completed properly .**