



DEAN ACADEMIC UG SECTION
DELHI TECHNOLOGICAL UNIVERSITY
Shahbad Daulatpur, Bawana Road, Delhi-110042

F. No. Dean UG/GIAN/2016-17/Notice/

Date: 21.09.2017

CIRCULAR

The following courses have been approved by National Coordinator, GIAN, IIT Kharagpur under GIAN phase II for Delhi Technological University in the year 2017-18. All the HODs are requested to circulate and display on the Notice board of the Department.

The Students of B.Tech, M.Tech, Ph.D, faculty members of academic institutions, engineers and researchers including R&D laboratories may apply for GIAN course. To register any of the following course, the candidates are advised to follow the following instructions.

- One time registration on GIAN portal <http://www.gian.iitkgp.ac.in/GREGN/index>, is required by paying on line Rs. 500/- and then any of the attached course (s) may be selected. Those who have already registered on the portal need not register again and have to only select the given course on GIAN portal.
- After registration on GIAN portal, the registered candidates are advised to submit the prescribed fee in the form of DD in favor of “Registrar, DTU” payable at Delhi along with printout of online submitted application form to the concerned Course Coordinator (GIAN).
- The shortlisted participants will be informed through e-mail. The course fee includes all instructional materials, computer use for tutorials and assignments and laboratory equipment usage charges. The course fee does not include boarding and lodging.
- The participants will be evaluated on the last day of the course and grade certificate based on 1 credit will be given to all the participants which is of immense importance for higher studies and other academic activities.

(Prof. Madhusudan Singh)
Local Coordinator, GIAN-DTU
Dean, Academics (UG)

Copy to:

1. PS to Hon'ble Vice Chancellor, DTU (for kind information)
2. PA to Pro-Vice Chancellor I, DTU
3. PA to Pro-Vice Chancellor II, DTU
4. Registrar, DTU.
5. All Deans
6. All HODs: With the request to circulate among faculty members and students of the department
7. Head, Computer Centre with request to upload on DTU website
8. All course coordinators (GIAN)

List of approved GIAN Courses in Phase II at Delhi Technological University in 2017-2018

S. No.	Course ID	Title	Speaker	Course Coordinator	Duration
1.	175015D03	Recent Trends in Power System Reliability Evaluation: Models, Statistical Methods and Applications	Prof. Chanan Singh, Texas A&M University, Texas, USA	Prof. Rachana Garg Department of Electrical Engineering	09.10.2017 to 13.10.2017
2.	175015D09	Emerging Cutting Edge Technologies in Advanced Electrical Machines and Drives: Design & Performance Issues, Fault Diagnosis, Failure Prognosis and Mitigation	Prof. Elias Strangas, College of Engineering, Michigan State University, USA	Dr. Mini Sreejeth Department of Electrical Engineering	06.11.2017 to 10.11.2017
3.	175015D15	Intelligent Transportation Systems	Prof. Miguel Angel Sotelo, University of Alcala Spain	Prof. S Indu Department of ECE	27.11.2017 to 01.12.2017
4.	175015D11	Emerging Trends in Advance Control System Applications in Intelligent Transport Systems	Prof. Markos Papageorgiou, Technical University of Crete, Greece	Sh. Ashish Rajeshwar Kulkarni Department of Electrical Engineering	04.12.2017 to 08.12.2017
5.	175015D16	SMART Power Flow Controller for Smart Grid Applications	Dr. Kalyan Sen, Sen Engineering Solutions, Inc., USA	Prof. Suman Bhowmick Department of Electrical Engineering	18.12.2017 to 23.12.2017
6.	175015D06	Recent trends in Protection of Microgrids with high DER penetration: Issues, Challenges and Mitigation	Mukesh Nagpal, BC Hydro Canada	Prof. Rachana Garg Department of Electrical Engineering	12.02.2018 to 16.02.2018
7.	175015D17	Video Surveillance	Prof. Bir Bhanu, Centre for Research in Intelligent Systems, USA	Prof. S Indu Department of ECE	June 2018 (Tentative)
8.	175015D01	Resilient power grid operation using synchrophosor technology: A smart grid operation perspective	Prof. Mani Vaithianathan Venkatasubramanian, Washington State University, USA	Prof. S. T. Nagarajan Department of Electrical Engineering	August 2018 (Tentative)