



DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009
(FORMERLY DELHI COLLEGE OF ENGINEERING)
BAWANA ROAD, DELHI-110042

Advt. No. DTU-MeitY-MCEV/EED/01

11 March 2020

ADVERTISEMENT TO FILL UP PROJECT POSITIONS

A walk in Interview for filling of project positions as per the details given below for the MeitY Sponsored R&D Project under Chief Investigator – Dr. Mini Sreejeth, Electrical Engineering Department (EED), Delhi Technological University (DTU) and Co-investigator – Prof. Indu S, Department of Electronics and Communication Engineering, DTU is scheduled to be held on **24 March 2020** in the Conference Hall of the EED

- Title of the Project** : Development and Deployment of motor controller for low to medium Power Electric Vehicles
- Sponsoring Agency** : Ministry of Electronics and Information Technology (MeitY)
- Details of Project Positions**

Sl. No	Designation	Minimum Qualification	Experience	Nos.	Monthly Emoluments (₹)
a)	Engineer	B.E. / B. Tech, in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering OR M.E. / M. Tech, (Power Electronics) with 1 st Division in both B.E. and M.E. levels Desirable : NET/ GATE Qualified	At least 5 years industrial experience in design and development of Inverters	1	80,000
b)	Technician	Diploma in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering	At least 2 years industrial experience in components soldering, pcb assembling and testing	1	17924

- Age Limit** : 40 years for Engineer and 35 years for Technician, relaxable up-to 5 years for candidates belonging to SC/ST/PH/OBC categories.
- Duration** : Appointment to the above post shall initially be for a period of six months, with further extension(s) of six month, based on performance, if deemed necessary, or for the duration of the project, whichever is earlier.

6. **Job description**

a)	Engineer	:	<ul style="list-style-type: none">- Embedded software development on 32 bits microcontroller platform for Inverter products- Software debugging- Excellent team work- Must have Experience on RS232, RS485 and CAN bus communication
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Mini Sreejeth
11/03/2020
Chief-Investigator

Indu S
Co-Investigator

Indu S
Professor & Head
Electrical Engineering Department
Delhi Technological University
(Formerly Delhi College of Engineering)
Bawana Road, Delhi-110042

b)	Technician	:	- Circuit assembling on general purpose PCB - Components soldering - Wiring
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7. Candidates before appearing for the Walk in Interview shall ensure that they are eligible for the position they intend to apply.
8. Candidates applying for the above post are requested to report on **24 March 2020 at 10:00 am** for registration, verification of documents, test / interview at the Department of EED, DTU. Candidates reporting after 10:30 am will not be entertained.
9. Candidates desirous to appear for the Walk in Interview should bring a set of self-attested copies of all certificates, testimonials, the latest CV and a passport sized colour photograph. The Application should include: -
 - Details of Diploma/degree/certificates obtained starting from Class X (or equivalent) clearly indicating the aggregate percentage / CGPA
 - Experience in research, industrial field, academics and others.
10. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of walk in interview for verification.
11. Please note that no TA/DA is admissible for attending the walk-in interview.
12. The interview will be held in Conference Hall of EED Department on **24 march 2020 at 10.30 am**.
13. The competent authority reserves the right to reject the candidature of any or all of the candidates without assigning any reason thereof.

Minisreejith
11/03/20 20

Chief Investigator

Jindal

Co-Investigator

[Signature]

Professor & Head
Electrical Engineering Department
Delhi Technological University
(Formerly Delhi College of Engineering)
Bawana Road, Delhi-110042