



DELHI TECHNOLOGICAL UNIVERSITY
Established by Govt. of Delhi vide Act 6 of 2009
(Formerly Delhi College of Engineering)
SHAHABAD DAULATPUR, BAWANA ROAD, DELHI-110042

No. F.DTU/ Rectt./ Astd P-ECE/2019/F-846/961

Dated: 13.02.2020

Advt.No. F.DTU/Rectt./Asso.Prof./Astd. Prof/2019 Dated 01.07.2019 and
Advt. No. F.DTU/Rectt./Astd. Prof/2019/269 Dated 11.09.2019

Schedule of Activities for recruitment to the post of Assistant Professor in the discipline of
Electronics & Communication Engineering

S.No.	Activity	Date
1	Uploading of Provisional list of 'Eligible', 'Exempted from written test' candidates and 'Not Eligible'	13.02.2020
2	Last date of receipt of Representations, if any	20.02.2020
3	Uploading of Revised list of 'Eligible', 'Exempted from written test' candidates and 'Not Eligible'	25.02.2020
4	Issue of Admit Cards for written test	26.02.2020
5	Date of written test	29.02.2020
6	Declaration of written test Result	01.03.2020
7	Physical Receipt of Filled proforma (Academic Record, Experience and Research Performance) and its verification, along with documentary proof	03.03.2020
8	Verification of relevant documents (X/XII/UG/PG marks, research, experience etc.) of candidates, exempted from written test plus qualified in written test and called for presentation & interview	to 07.03.2020
9	Presentation Schedule	12.03.2020 to 15.03.2020
10	Interview Schedule	14.03.2020 to 17.03.2020

NOTE: -The candidates are advised to keep visiting the University website regularly for updates. No separate communication may be done with regard to schedule and other activities related to the recruitment process

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Registrar