



Delhi Technological University

(Formerly Delhi College of Engineering)
(Established under Delhi Act 6 of 2009, Govt. of NCT of Delhi)
Shahbad, Daulatpur, Bawana Road-Delhi-110042, India.

JOB DESCRIPTION

Applications are invited for one **Junior Research Fellow (JRF)** and one **Project Assistant (PA)** position from highly motivated and eligible candidates for **DST** funded sponsored research cum demonstration project entitled “**Design and Demonstration of Off-Grid Self Healing & Sustainable DC Community Energy Solutions**” under Mission Innovation scheme:

Duration of the project: 2 Years (wef. December 2018)

1(a) Essential Qualification for JRF:

- (i) M. Tech. or M.E. degree or higher in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering with specialization in Communication, Embedded Systems and/or IOT Applications with first division (desirable GATE score).
- (ii) B. Tech. or B.E. degree in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering with first division (minimum 60%) and a GATE score (mandatory). **However, final year B.Tech/B.E students are also eligible to apply, provided they have 75% marks/equivalent CGPA (without any backlog) up to 7th semester of the eight semester degree program, and a valid GATE score. Their appointment shall be confirmed only after their production of the proof from the respective University/Institute about their passing all courses mandated for B.Tech/B.E degree with 75% marks. Last date for making such arrangement shall be 31st July, 2019.**

1(b) Essential Qualification and Experience for PA:

- (i) B.Tech or B.E degree in Electrical Engineering / Electrical and Electronics Engineering with with first division (minimum 60%). **However, final year B.Tech/B.E students may also eligible to apply, provided they have 60% marks/equivalent CGPA (without any backlog) up to 7th semester of the eight semester degree program. Their appointment shall be confirmed only after their production of the proof from the respective University/Institute about their passing all courses mandated for B.Tech/B.E degree with 60% marks. Last date for making such arrangement shall be 31st July, 2019.**

2. The candidates with one year relevant hands on experience will be preferred.

It is highly desirable that the candidates for JRF should have research / hands on experience in architecture, design, operation and control of communication system, networking and network management systems. Further it is desirable that candidate should be well versed with Linux operating system.

For the post of PA the knowledge and hands on experience on integration of renewable energy sources with grid etc, and hands on experience in developing and working on hardware / laboratory prototype of the converters, PCB design software besides knowledge of microcontroller/DSP programming would be preferable.

Job Profile: The candidates will have to develop and work on hardware/laboratory prototype of the converters/communication interface/ network management systems.

Applicants can submit their CV's along with their **skype id (mandatory)** at dtu.simlab@gmail.com latest by **13th July, 2019 (Saturday)**.

Date of Interview: 16th July, 2019 (Tuesday).

**No. of Vacancies – 01 (JRF)
01 (PA)**