

Conference/Seminar/Symposia/Workshops- ECE, DTU

S. No.	Faculty	Title of Programme	Sponsoring	Duration
1.	Dr. Sumit Kale	Emerging Nanoscale Devices, Circuits and Their Applications	ECE, DTU	10 th May -14 th May 2021
2.	Prof. S. Indu, Dr. Chhavi Dhiman, & Dr. Rohit Kumar	Machine Learning for Wireless Networks and Computer vision	ECE, DTU	11 th July - 29 th July 2022
3.	Prof. S. Indu and Prof. Gurjit Kaur	IEEE IoT Workshop: The future of Connectivity	IEEE Delhi CS Chapter, IEEE Delhi ComSoc Chapter	28 th -29 th October 2021
4.	Dr. Chhavi Dhiman	Recent advances and challenges in Computer vision	ECE, DTU	3 rd - 7 th December 2021
5.	Prof. Gurjit Kaur & Dr. Rohit Kumar	Machine Learning Applications in Signal Processing and Wireless Networks	ECE, DTU	20 th -24 th December, 2021
6.	Prof. S. Indu & Prof. Gurjit Kaur	Wireless Sensor Networks and IoT	ATAL FDP	10 th - 14 th January 2022
7.	Dr. Sumit Kale, Dr. Sonam Rewari, & Dr. Sachin Dhariwal	Webinar on Solar Cell and OLED Modelling and Simulation using SETFOS S/W		15 th March 2022
8.	Mr. Pankaj Dahiya & Dr. Anukul Pandey	EXPLORING ENGINEERING	Centre for Extension and Field outreach	20 th to 25 th June 2022
9.	Prof. S. Indu & Dr. N. Jayanthi	Blockchain Technologies: Introduction to Web 3.0 and Development on Decentralized Blockchain Networks	IEEE Delhi CS Chapter	12 th October 2023
10.	Prof. S. Indu & Dr. N. Jayanthi	Seminar on Emerging Transportation Solutions driven by Industry 4.0 Transformation	IEEE Delhi CS Chapter	11 th December 2023
11.	Prof. S. Indu & Dr. N. Jayanthi	One-day seminar on Understand Intelligence and artificial Intelligent	ECE, DTU	20 th March 2024
12.	Dr. Sumit Kale, Dr. Sonam Rewari, & Dr. Sachin Dhariwal	Advancement and Challenges in VLSI Design and Nanoscale Devices	ECE, DTU	27 th - 31 st March 2024

13.	Prof. S. Indu & Dr. N. Jayanthi	Invited Talk on AI-Driven Robotics	IEEE ITSS Delhi Chapter	14 th August 2024
14.	-	Aging Issues and Compact Modelling of Nano transistor for Hardware Security Primitives	SERB	November 2024
15.	Dr. Sumit Kale	One week Faculty Development Program on Recent Trends in Semiconductor Technology and Chip Design	ECE, DTU	2 nd -7 th December 2024
16.	Lecture Series:1	Telecom and Networking-The Big Picture	ECE, DTU	27 th March 2025
17.	Mr. Akshay Mann	NXP: Campus Connect Webinar: Getting familiar with i.MX8MP FRDM	ECE, DTU	19 th August 2025
18.	Mr. Kaustubh R. Singh	IEEE 5G/6G Testbed Demonstration Webinar	IEEE	21 st August 2025
19.	Lecture Series:2	Telecom Core Network Architecture	ECE, DTU	11 th September 2025
20.	Lecture Series:3	Latest Trends in Semiconductor Design and Verification	ECE, DTU	10 th October 2025

Blockchain Technologies: Introduction to Web 3.0 and Development on Decentralized Blockchain Networks



Seminar on Emerging Transportation Solutions driven by Industry 4.0 Transformation



IEEE-ITSS Delhi Chapter and Department of Electronics and Communication Engineering, Delhi Technological University, Delhi is organizing a Seminar on Emerging Transportation Solutions driven by Industry 4.0 Transformation

Date & Time: 11th December 2023, 3:00 - 5:00 p.m
Venue: Committee Room, ECE Dept., Delhi Technological University, Delhi

Abstract: Industry 4.0 Transformation, including 5G connectivity, crowdsourcing, social networking, big data analytics, artificial intelligence, cloud-edge computing, and immersive technologies, has completely changed the landscape of several industries and is fundamentally changing how we understand, measure, and control transportation systems. In this talk, I will share examples of how I have designed solutions based on 4.0 revolutions to better understand driver behavior, improve traffic operations, and move towards autonomy under extreme conditions.

Speaker: Prof. Anuj Sharma, Iowa State University, USA



Anuj Sharma, PhD, is a professor in the Department of Civil, Construction, and Environmental Engineering (CCEE) at Iowa State University (ISU). Dr. Sharma's research has been recognized by numerous funding agencies, including the National Science Foundation, the Federal Highway Administration, the National Institute of Health, several state departments of transportation (DOTs), and multiple city public works departments. Dr. Sharma has been granted awards of more than \$22 million, contributing to his research. He has served as principal investigator/co-principal investigator for over 60 research projects over 14 years. Dr. Sharma has over 150 conference/journal publications and served as an editor-in-chief for Springer's Journal of Big Data Analytics in Transportation. He is also on the organizing committee of the AI City Challenge, which provides a platform for data scientists worldwide to design state-of-the-art Machine Learning models to solve smart-city problems. His research uses big data-driven discoveries to help enable better short-term (automated control) and long-term (policy) decisions. Dr. Sharma instrumented and co-led the REACTOR (Real-Time Analytics of





Prof. S. Indu
Treasurer, ITSS Delhi Chapter
Professor, Department of Electronics and Communication Engineering,
Dean, Student Welfare
Delhi Technological University, Delhi.



Dr. Ayesha Choudhary
Chair, ITSS Delhi Chapter
Assistant Professor
School of Computer and Systems Sciences,
Jawaharlal Nehru University,
New Delhi.

Organising Committee Members

1. Prof. J. Panda, Department of ECE, DTU.
3. Dr. Bhavneesh Jain, Department of ECE, DTU.

2. Dr. N. Jayanthi, Department of ECE, DTU.
4. Dr. Ashish Kulkarni, Department of EE, DTU.

One-day seminar on Understand Intelligence and artificial Intelligent



Advancement and Challenges in VLSI Design and Nanoscale Devices



Invited Talk on AI-Driven Robotics



Webinar on Solar Cell and OLED Modelling and Simulation using SETFOS S/W



Department of Electronics & Communication Engineering
Delhi Technological University, Delhi-42



**Organizes a Webinar on
Solar Cell and OLED Modelling and Simulation Using SETFOS S/W**

Day & Date: Tuesday, 15th March 2022
Time: 11:00 AM to 12:30 PM

Chief Patron
Prof. J. P. Saini
Vice Chancellor, DTU



Scan QR Code to Join

No Registration/Participation Fees



Resource Person
Mr. Anil Sharma
Managing Director
Impulse Technology Gurgaon (Haryana)

Co-Patron
Prof. N. S. Raghava
HOD (ECE), DTU

Meet link:
<https://meet.google.com/vbe-wkid-rht>

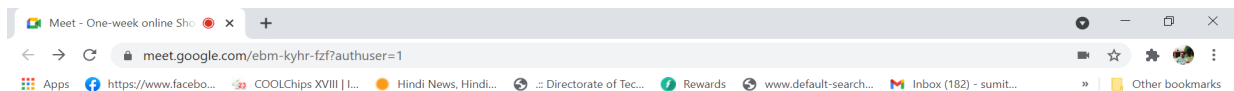
Coordinators
Mr. Sachin Dhariwal
Assistant Professor

Dr. Sonam Rewari
Assistant Professor

Dr. Sumit Kale
Assistant Professor



One-week online Short-Term... 5:24 PM



EFFECT OF INTERFACE TRAP (N_0 & σ) VARIATION

(a) Acceptor $\mu V_{oc}/V_{oc}$ vs. Trap density peak energy, E_t (eV). $N_0 = 10^{10}$ (red), 10^{11} (blue), 10^{12} (green). $\sigma = 0.2$ (circles), 0.1 (triangles), 0.05 (squares).

(b) Acceptor $\mu V_{oc}/V_{oc}$ vs. Trap density peak energy, E_t (eV). $N_0 = 10^{12}$ cm⁻²eV⁻¹. $\sigma = 0.05$ (circles), 0.1 (triangles), 0.15 (squares), 0.2 (diamonds).

(c) Donor $\mu V_{oc}/V_{oc}$ vs. Trap density peak energy, E_t (eV). $N_0 = 10^{10}$ (red), 10^{11} (blue), 10^{12} (green). $\sigma = 0.2$ (circles), 0.1 (triangles), 0.05 (squares).

(d) Donor $\mu V_{oc}/V_{oc}$ vs. Trap density peak energy, E_t (eV). $N_0 = 10^{12}$ cm⁻²eV⁻¹. $\sigma = 0.05$ (circles), 0.1 (triangles), 0.15 (squares), 0.2 (diamonds).

Below E_F acceptor traps get -ve charged by accepting electron (filled state) and above E_F neutral (empty state).
Above E_F donor traps get +ve charged by donating electron (empty state) and below E_F neutral (filled state).
As the N_0 (10^{10} - 10^{13} cm⁻²eV⁻¹) and σ (0.05-0.2) increases, the effective interface trap concentration in the SC bandgap region increases and the V_T fluctuations increases for both acceptor and donor traps.

[14] Sudarsanan, A. and Nayak, K., *Journal of Computational Electronics*, 2021, pp.1[24]

One-week online Short-Term... K Nayak is presenting 12:27 PM



**One week online Short Term Training Programme on Emerging Nanoscale Devices,
Circuits and its Application**

Reminder to give an expert talk x Meet - ebm-kyhr-fzf x

meet.google.com/ebm-kyhr-fzf

REC sachin dharwal is presenting D Dipti Srivastava and 31 more 42 11:01 AM You

PowerPoint Slide Show - (STTP_Day-3.pptx) - PowerPoint

One Week Online Short Term Training Programme On Emerging Nanoscale Devices, Circuits and Its Applications (NANODC-21) 10th May 2021 to 14th May 2021

Topic: High Performance Organic Transistor and Memory for Flexible Electronics Applications

Organized by
Department of Electronics & Communication Engineering
Delhi Technology University (Formerly Delhi College of Engineering), Shahabad Daulatpur
Main Bawana Road, Delhi-110042

Day-3
Wednesday 11-1 PM
12th May 2021

Speaker
Dr. Shree Prakash Tiwari
Associate Professor, IIT Jodhpur

Coordinator: Dr. Sumit Kale
Convener: Prof. N.S. Raghava
Co-Coordinator: Mr. Sachin Dharwal

Meeting details ^

Turn on microphone (ctrl + d)

Raise hand Turn on captions sachin dharwal is presenting

Type here to search

Meet - ebm-kyhr-fzf

meet.google.com/ebm-kyhr-fzf

REC You Sumit Kale CIPET RANCHI

Anurakshree Verma sachin dharwal A. K. Singh

ADITYA VORA has left the meeting Ur. Manoj Kumar Amandeep Sagwal Bhawna Saini

Meeting details x

People (93) Chat

what is frequency(range) of operation

Dr D AJITHA 12:33 PM
Very informative session sir
Thank you

Dr. Liton Balo 12:33 PM
Thank you for this presentation

Kirthika Rishikeshav, Akil 12:33 PM
Very informative session sir

Dr D AJITHA 12:34 PM
Frequency of operation sir

Send a message to everyone

Meeting details ^

Raise hand Turn on captions Present now

Type here to search

Telecom and Networking- The Big Picture



One week Faculty Development Program on Recent Trends in Semiconductor Technology and Chip Design





Aging Issues and Compact Modelling of Nano transistor for Hardware Security Primitives

Meena Panchore (Presenting)

SERB SPONSORED WORKSHOP
Aging Issues and Compact Modelling of
Nanotransistor for Hardware Security Primitives

Presented By:
Dr. Meena Panchore
Assistant Professor

Department of Electronics & Communication Engineering
NIT Patna

11/19/2024

2:24 PM | One Day Online Workshop o...

Meena Panchore, Roma Gupta, Madhulika Verma, Vaibhav Nijhawan, Sweta Choudhary, Ishita Pathak, Anubha Goel, 8 others, Sumit Kale

Dr. Bhaskar Awadhiya (Presenting)

Cont..

Fig 1.3: (a) V_{MOS} vs V_G (b) Transfer characteristics for NCFET

$V_{MOS} = \frac{|C_F|V_G}{|C_F| - C_{MOS}} + \frac{-C_D V_D}{|C_F| - C_{MOS}}$ At $V_G = 0$; V_{MOS} is negative and at $V_G = 0.9$, $V_{MOS} > V_G$

4:42 PM | One Day Online Workshop o...

Dr. Bhaskar Awadhiya, Madhulika Verma, Malvika Malvika, Shalini Singh, 23/PHDECE/507 P..., Ishita Pathak, PRANAV KUMAR 1..., 7 others, Sumit Kale

11:02 AM | One Day Online Workshop o...

Tarvika Garg, Vijay Thakur, Anubha Goel, Priyank Sharma, Malvika Malvika, Pranav Kumar, Madhulika Verma, 9 others, Sumit Kale

Sumit Kale (Presenting)

Subthreshold leakage current

Subthreshold leakage current is typically dominated by three components.

- **Gate-induced drain-leakage current (I_{GIDL}) [2]**
 I_{GIDL} is caused by the tunneling of electrons through the relatively large but thin barrier at the drain side in off state.
- **Junction leakage (I_j) [2]**
 I_j is caused by the reverse-biased Schottky junction at the drain electrode.
- **S/D thermal-emission leakage current (I_{th}) [2]**

Sumit Kale

Vijay Thakur

Anubha Goel

Malvika Malvika

Dr. Dharmendr...

AMIT AGARWAL

Priyank Sharma

16 others

Tanvika Garg

9:55 AM | exf-ffko-dmq

One Day Online Workshop o...

Tanvika Garg

Vijay Thakur

Sweta Choudhary

Ishita Pathak

Rohit Khandekar

Madhulika Verma

AMIT AGARWAL

9 others

Sumit Kale

9:15 AM | One Day Online Workshop o...

Department of Electronics and Communication Engineering, developed a Departmental ERP Portal as part of their final year project



NXP Campus Connect
Webinar: Getting familiar with i.MX8MP FRDM





IEEE 5G/6G Testbed Demonstration Webinar

IEEE in collaboration with Dept. of Electronics and Communication Engineering

& Dept. of Information Technology
Delhi Technological University



IEEE 5G/6G

Testbed Demonstration Webinar

Date- 21/08/2025

Time- 05:00PM -06:30PM

Link- <https://meet.google.com/uza-xaqv-vbp>

Faculty Coordinators:

Dr. Anamika Chauhan (IT)

Mr. Kaustubh R. Singh (ECE)

Webinar Registration Details:

Fees- Nil for UG/PG/PhD Scholars and Faculty.

Registration: <https://forms.gle/AfBiQzXGovadvMmc9>

E-Certificates will be provided to attendees.

Prof. Prateek Sharma
Hon'ble VC, DTU
(Chief Patron)



Prof. Narendra Kumar
Registrar DTU
(Patron)



Prof. Neeta Pandey
(HoD ECE)



Prof. Dinesh Vishwakarma
(HoD IT)



Lecture Series 2: Telecom Core Network Architecture

 **Dept. of Electronics
and
Communication Engineering**
DELHI TECHNOLOGICAL UNIVERSITY

 विकसित भारत
अभियान
1947-2047

Presents

**Telecom
Core Network Architecture**

Lecture Series in Electronics Engineering
11th SEPTEMBER, 2025 **2nd LECTURE**



Mr. K. V. Syam Sunder
Associate Vice President,
Network Digitization at Bharti Airtel

 **3:00 PM
Onwards**  **Pragyan Hall, 2nd Floor
Admin. Building, DTU**



Lecture Series 3: Latest Trends in Semiconductor Design and Verification



Dept. of Electronics and Communication Engineering
DELHI TECHNOLOGICAL UNIVERSITY



Presents

Latest Trends in Semiconductor Design and Verification

Lecture Series in Electronics Engineering

10th OCTOBER, 2025

3rd LECTURE



Mr. Puneet Kumar
Sr. Director, R&D
Synopsys (India) Pvt. Ltd.

11:00 AM Onwards

Pragyan Hall, 2nd Floor
Admin. Building, DTU





IMC 2025 Visit to Yashobhoomi Dwarka

