S.No		1	1							
		Course Code & Title	1.	Course Outcomes y to apply knowledge of mathematics, science, and engineering to the analysis and Co	omprehend	hasic nrinc	inles of V	octor Analy	reie euch ac	Coordinate
				ms and transformations, divergence, gradient and curl operators.	omprenena	Dasic princ	ipies oi vi	ector Arialy	/SIS SUCIT ds	Coordinate
	1	EE 262 & Electro Magnetics	EE262.2	ze and apply laws and methods to solve problems in electro-statics and magneto-sta	tics.					
			-	/ Maxwell's equations to solve problems in electromagnetics. date, formulate and analyze electromagnetic wave propagation in various kinds of me	edia					
			-	late parameters/metrics in problems involving transmission lines and waveguides.	- uiui					
			1	Program (PO/PSO)Articulation Matrix						
					ı					1
S.No		Course Code & Title		Course Outcomes						
	2	EC202 & Analog Electronics		ify the high frequency limitations of BJTs and JFETs/MOSFETs and determine frequen	icy response	of single a	nd multist	tage ampli	fiers.	
İ			-	in the concept of and analyze the performance of negative feedback circuits ribe the concept of positive feedback and criterion for oscillations, analyse and design	n different B	JT based os	scillators			
İ			EC202.4	rentiate between the voltage, current and power amplifier and design the power amp						
İ				ribe the fabrication process of devices. In, construct and take measurement of various analog circuits and compare experime	ental results	in the laho	ratory wit	th theoretic	ral analysis	
			LC202.0	and take measurement of various analog enealts and compare experime	ca resures	iii tiic idbo	ratory wit	ar theoret.	car anarysis:	
S.No		Course Code & Title		Course Outcomes						
				n the combinational and sequential digital circuits using VHDL/Verilog						
	3	EC-204 & Digital Design II	-	ement state tables and circuits using Finite State Machine rate state minimization and hazards for Synchronous and asynchronous sequential ma	achines	1				
İ				lify races and faults for asynchronous sequential machines		1			l l	- I
-			EC204.5	n the Programmable Logic Devices and realization of algorithmic state machines.						
S.No		Course Code & Title		Course Outcomes						
1				prehend introductory principles of communication systems such as types of signals an						
1	4	EC 206 & Communication Systems Course Code & Title	-	date the process of amplitude, frequency and phase modulation and describe the des	ign of their	transmitte	rs, and red	ceievers		
	4			/ sampling theory and analyze pulse code modulation and delta modulation / the knowledge of random variables and processes to study noise in communication	systems					
C No				pare the noise performance and design tradeoffs of various modulation schemes	_		_		_	
S.No		Course Code & Title	EC208.1	Course Outcomes ify the organization and structure of digital computer including differentiation of harc	dwired and	microprogr	ammed co	ontrol unit	S.	
1	5	EC208& Computer architecture	EC208.2	lop computer arithmetic algorithms and their hardware implementation.		, -3				
				nize and interface different types of memories and input output devices. ate the system performance using pipelining and parallel processing.						
				in a basic computer system.						
S.No		Course Code & Title		Course Outcomes						
3.140		EC302&VLSI Design	EC302.1	ribe the technology, design concepts, electrical properties and modelling of MOS devi	ice					
	7			ct the impact of scaling on MOS device.						
				rze static and timing parameters of inverters and design inverters as per specification in static combinational logic circuits using CMOS, Transmission gates and complement		ansistor log	ic			
				dynamic circuit techniques to combinational circuit design	, , , , , , , , , , , , , , , , , , , ,		,,-			
İ			EC302.6	n and analyze Static and Dynamic Sequential logic circuit and evaluate timing parame	eters					
İ				rstand VLSI design methodologies and implementation methods						
			EC302.8	pare experimental results in the laboratory with theoretical analysis	1			-		1
S.No		Course Code & Title		Course Outcomes	-	1				
	8	EC304 & Digital signal	EC304.1	prehend the basics of a DSP system such as digital signal processing and analog to digi	ital conversi	on				
İ					itai conversi					
	8			lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form						
i	8	EC304 & Digital signal Processing	EC304.3 EC304.4	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method	ns and other st	tandard me				
S.No	8		EC304.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications	ns and other st	tandard me		t of finite v	vord length	effect in
S.No	8	Processing	EC304.3 EC304.4 EC304.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method	ns and other st	tandard me		t of finite v	vord length	effect in
S.No	8	Processing Course Code & Title	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in thier direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes prehend basic principles of a microcontroller and identify its need. Ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr	ns and other st s, Understa rogramming	tandard me anding of th skills using	ne concep			effect in
S.No	9	Processing	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method idderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need.	ns and other st s, Understa rogramming	tandard me anding of th skills using	ne concep			effect in
S.No		Processing Course Code & Title EC 306 & Embedded	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. Ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Micr	and other si s, Understa rogramming	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
S.No		Processing Course Code & Title EC 306 & Embedded	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes orehend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pribe the internal architecture and interfacing of different peripheral devices with Microtet the need and working principles of Digital Signal Processors and their variants	and other si s, Understa rogramming	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
S.No		Processing Course Code & Title EC 306 & Embedded	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method, iderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes prehend basic principles of a microcontroller and identify its need. ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pribe the internal architecture and interfacing of different peripheral devices with Microte the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti	and other si s, Understa rogramming	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
		Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm n, analyse and implement FIR and Ill Rsystems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes ne testing techniques for digital circuits	and other si and other si s, Understa rogramming rocontrollers	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
		Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5 EC310.1 EC310.1 EC310.2 EC310.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method inderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. iffy basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microcate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introduction Course Outcomes the testing techniques for digital circuits interesting techniques for digital circuits interesting and diagnosis of faults rentiate between testing and diagnosis of faults	and other si and other si s, Understa rogramming rocontrollers	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5 EC310.1 EC310.1 EC310.3 EC310.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and Ill Rsystems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method . iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microcate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes the testing techniques for digital circuits ine a digital circuit for various types of faults rentate between testing and diagnosis of faults in a self-testing circuit.	ns and other sis, Understa s, Understa sogramming rocontroller.	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5 EC310.1 EC310.1 EC310.3 EC310.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. In the Filters with the strength of th	ns and other sis, Understa s, Understa sogramming rocontroller.	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.4 EC306.5 EC310.1 EC310.1 EC310.3 EC310.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm in, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method, iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes Tourse Outcomes Tourse Outcomes Tourse Outcomes Tourse Outcomes The basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate price the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants as memory organization of ARM Microcontroller and its variants, RT-Linux introductions Course Outcomes Tourse Outcomes	ns and other sis, Understa s, Understa sogramming rocontroller.	tandard me anding of the skills using s.	PIC and A	ARM micro	controller.	
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.5 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm n, analyse and implement FIR and Ill Rystems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Micr rate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes the testing techniques for digital circuits ine a digital circuit for various types of faults the rentate between testing and diagnosis of faults the aself-testing circuit Training the efficacy of a given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The training techniques for digital circuits of the course of the processor o	ns and other sta and other sta sta state of the state of	tandard me anding of th skills using s.	e concept PIC and A	5cheduling	Bus structu	re
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.3 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes rehend basic principles of a microcontroller and identify its need. Ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes the testing techniques for digital circuits ine a digital circuit for various types of faults tentiate between testing and diagnosis of faults in a self-testing circuit Tentine the efficacy of a given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The render of schedule based and random Medium Access Control protoco et overhead.	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.3 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes rehend basic principles of a microcontroller and identify its need. Ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes the testing techniques for digital circuits ine a digital circuit for various types of faults tentiate between testing and diagnosis of faults in a self-testing circuit Tentiate between testing and diagnosis of faults in a self-testing circuit Tentiate between testing and given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The e	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor	EC304.3 EC304.4 EC304.5 EC304.5 EC306.3 EC306.3 EC306.4 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. In the processing and develop its applications of the processing and develop its applications. Course Outcomes or the order of the processing and develop its applications or the processing and develop its applications. If you have the internal architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate principles architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate principle internal architecture and interfacing of different peripheral devices with Microcontroller and demonstrate principles are the need and working principles of Digital Signal Processors and their variants as memory organization of ARM Microcontroller and its variants', RT-Linux introductions are testing techniques for digital circuits Course Outcomes on testing techniques for digital circuits are a self-testing circuit remine the efficacy of a given testing algorithm, Detect the faults in synchronous circuit architecture of a given testing algorithm, Detect the faults in synchronous circuit remine the efficacy of a given testing algorithm, Detect the faults in synchronous circuit architecture architecture and applications or an order of the process of the performance of schedule based and random Medium Access Control protoco at overhead. ate the performance of transport control protocols for congestion detection and avoits as the WSN routing issues by considering QoS measurements.	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor	EC304.3 EC304.4 EC304.5 EC304.5 EC306.3 EC306.3 EC306.4 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes rehend basic principles of a microcontroller and identify its need. Ify basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes the testing techniques for digital circuits ine a digital circuit for various types of faults tentiate between testing and diagnosis of faults in a self-testing circuit Tentiate between testing and diagnosis of faults in a self-testing circuit Tentiate between testing and given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The efficacy of a given testing algorithm. Detect the faults in synchronous circuit Course Outcomes The e	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks	EC304.3 EC304.4 EC304.5 EC304.5 EC306.2 EC306.3 EC306.5 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.4 EC310.5 EC310.5 EC310.5 EC310.5 EC310.5 EC310.5 EC310.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Siters using windows, sampling methods, impulse invariance method. In the strength of the	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	9	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.2 EC306.3 EC306.3 EC306.4 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC316.1 EC316.5 EC316.6 EC316.6 EC316.6 EC316.6 EC316.6 EC316.6 EC316.7 EC316.7 EC316.7 EC316.8 EC316.8	lementation of Z-transform, DFT, FFT and implementation of various algorithm (n, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes orehend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pribe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introductions are testing techniques for digital circuits Course Outcomes The testing techniques for digital circuits The as self-testing circuit for various types of faults The astignation of the self-testing and diagnosis of faults The mine the efficacy of a given testing algorithm, Detect the faults in synchronous circuit from the efficacy of a given testing algorithm, Detect the faults in synchronous circuit from the efficacy of a given testing algorithm, Detect the faults in synchronous circuit from the efficacy of a given testing algorithm, Detect the faults in synchronous circuit from the efficacy of a given testing algorithm, Detect the faults in synchronous circuit are the performance of schedule based and random Medium Access Control protocols overhead. The performance of transport control protocols for congestion detection and avoid set the performance of transport control protocols for congestion detection and avoid set the performance of transport control protocols for congestion detection and avoid set the performance of transport control protocols for congestion detection and avoid set the performance of transport control protocols for congestion detection and avoid set the performance of transport control protocols for congestion detection and a	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC316.1 EC316.2 EC316.3 EC316.4 EC316.2 EC316.3 EC316.4 EC316.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Siters using windows, sampling methods, impulse invariance method. In the strength of the	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.3 EC306.3 EC306.3 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC316.3 EC316.4 EC316.5 EC316.5 EC316.3 EC316.4 EC316.5 EC316.3 EC316.4 EC316.5 EC402.3 EC402.1 EC402.2 EC402.3 EC402.4	lementation of Z-transform, DFT, FFT and implementation of various algorithm n, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title	EC304.3 EC304.4 EC304.5 EC306.1 EC306.2 EC306.2 EC306.3 EC306.5 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC400.2 EC400.2 EC400.2 EC400.2 EC400.2 EC400.2 EC400.2 EC400.2 EC400.2	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and Ili Rystems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method in ging FIR and IIR Filters using windows, sampling methods, impulse invariance method inderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes or the processing and develop its applications or the process of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pribe the internal architecture and interfacing of different peripheral devices with Microcate the need and working principles of Digital Signal Processors and their variants ses memory organization of ARM Microcontroller and its variants', RT-Linux introduction of the strain of the second o	and other si and other si s, Understa cogramming rocontroller: ion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10 11 13	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.3 EC306.5 EC306.3 EC306.4 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.4 EC310.5 EC316.1 EC316.5 EC316.1 EC316.2 EC316.3 EC316.4 EC316.4 EC316.5 EC402.1 EC402.2 EC402.3 EC402.4 EC402.4 EC402.4 EC402.4 EC402.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm n, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. Inderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orchend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introduction Course Outcomes The testing techniques for digital circuits The aself-testing circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficacy of a given testing algorithm, Detect the faults in synchronous circuit to the efficiency of a given testing algorithm, Detect the faults in synchronous circuit to the efficiency of a given testing algorithm, Detec	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title EC402 & B.Tech Project-II	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.3 EC306.3 EC306.4 EC306.5 EC306.4 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.5 EC310.4 EC310.5 EC310.5 EC310.6 EC402.1 EC402.2 EC402.3 EC402.4 EC402.4 EC402.4 EC402.4 EC404.1 EC404.1	lementation of Z-transform, DFT, FFT and implementation of various algorithm n, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications Course Outcomes orehend basic principles of a microcontroller and identify its need. Iffy basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pr ibe the internal architecture and interfacing of different peripheral devices with Micr rate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introducti Course Outcomes The testing techniques for digital circuits frentiate between testing and diagnosis of faults frentiate between testing and diagnosis of faults frentiate between testing and diagnosis of faults frentiate between testing and diagnosis of faults frentiate between testing and diagnosis of faults frentiate between testing and paper testing algorithm, Detect the faults in synchronous circuit Course Outcomes The tentian of the performance of schedule based and random Medium Access Control protoco et overhead. The performance of transport control protocols for congestion detection and avoid ss the WSN routing issues by considering QoS measurements. The tentian of the performance of transport control protocols for congestion detection and avoid ss the WSN routing issues by considering QoS measurements. The performance of transport control protocols for congestion detection and avoid ss the WSN routing issues by considering QoS measurements. The performance of transport control protocols for congestion detection and avoid ss the WSN routing issues by considering QoS measurements. The performance of transport control protocols for congestion detection and avoid as the performance of transport control protocols for congestion detection and avoi	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No S.No	10 11 13	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title EC402 & B.Tech Project-II Course Code & Title EC404 & Wireless Communication	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.3 EC306.3 EC306.4 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC316.3 EC316.4 EC316.5 EC402.1 EC402.1 EC402.2 EC402.3 EC402.4 EC402.5 EC402.4 EC402.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. In the strength of the	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No	10 11 13	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title EC402 & B.Tech Project-II Course Code & Title	EC304.3 EC304.4 EC304.4 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.4 EC306.5 EC306.3 EC306.4 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.4 EC310.5 EC310.6 EC310.5 EC310.6 EC310.	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Siters using windows, sampling methods, impulse invariance method iderstand the basics of multirate digital signal processing and develop its applications. Course Outcomes orehend basic principles of a microcontroller and identify its need. If y basic architecture of a 8-bit, 16-bit and 32 bit Microcontroller and demonstrate pribe the internal architecture and interfacing of different peripheral devices with Microtate the need and working principles of Digital Signal Processors and their variants ss memory organization of ARM Microcontroller and its variants', RT-Linux introductions are testing techniques for digital circuits Course Outcomes The testing techniques for digital circuits The testing techniques for digital circuits The testing techniques for digital circuits The testing techniques for digital circuits The testing direction of the testing and diagnosis of faults The as self-testing circuit The aside testing and diagnosis of faults The aside testing and diagnosis of faults The aside testing circuit The efficacy of a given testing algorithm, Detect the faults in synchronous circuit Course Outcomes The testing circuit of the performance of schedule based and random Medium Access Control protocols of overhead. The performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control protocols for congestion detection and avoid the performance of transport control pr	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No S.No	10 11 13	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title EC402 & B.Tech Project-II Course Code & Title EC404 & Wireless Communication Course Code & Title	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.3 EC306.3 EC306.3 EC306.4 EC306.5 EC310.1 EC310.2 EC310.3 EC310.4 EC310.5 EC316.3 EC316.4 EC316.2 EC316.3 EC316.4 EC316.5 EC402.1 EC402.1 EC402.1 EC402.3 EC402.4 EC402.3 EC402.4 EC402.5 EC402.3 EC402.4 EC402.5 EC402.5 EC402.5 EC402.5	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IIR systems in their direct, cascade and lattice form ign FIR and IIR Filters using windows, sampling methods, impulse invariance method. In the strength of the	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control
S.No S.No	10 11 13	Processing Course Code & Title EC 306 & Embedded Systems Course Code & Title EC 310 & Testing and diagonis of Digital System Design Course Code & Title EC316 & Wireless sensor Networks Course Code & Title EC402 & B.Tech Project-II Course Code & Title EC404 & Wireless Communication	EC304.3 EC304.4 EC304.5 EC304.5 EC306.1 EC306.2 EC306.2 EC306.3 EC306.3 EC306.4 EC306.5 EC310.4 EC310.2 EC310.3 EC310.4 EC310.4 EC310.5 EC316.1 EC316.1 EC316.1 EC316.2 EC316.3 EC316.4 EC316.4 EC316.5 EC402.1 EC402.2 EC402.3 EC402.3 EC402.4 EC402.4 EC404.1 EC404.5 EC404.5 EC404.1 EC404.1 EC404.1 EC404.1 EC404.1 EC404.1 EC404.1 EC404.1 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3 EC404.3	lementation of Z-transform, DFT, FFT and implementation of various algorithm m, analyse and implement FIR and IR systems in their direct, cascade and lattice form iging FIR and IR Filters using windows, sampling methods, impulse invariance method. In the strength of the	and other si and other si s, Understa rogramming rocontroller sion, RTOS ke	skills using s.s.	eal-Time S	Scheduling	Bus structu	re and control

S.No	Course Code & Title		Course Outcomes
		EC422.1	To describe the mathematical modelling of robotics and understand concept of homogenous transformation.
		EC422.2	To evaluate forward kinematic models and understand kinematic constraints and mobile robot maneuverability and motion control.
16	EC422& Robatics and machine vision		To categorize the classification of sensors and evaluate various performance characteristics of sensor and understand the issues with sensor noise.
		EC422.4	To analyze the feature extraction and object recognition methods and algorithms for the tracking of objects
		EC422.5	To describe the localization requirement for mobile robot and their challenges and path planning and navigation.
S.No	Course Code & Title		Course Outcomes
	EC412 & Machine Learning	EC412.1	Describe the fundamental issues and challenges of machine learning: data, model selection, model complexity, etc
		EC412.2	Explain the underlying mathematical relationships within and across Machine Learning algorithms.
17		EC412.3	Compare and contrast different paradigms for learning (supervised, unsupervised, etc.).
		EC412.4	Evaluate the models and optimize the models learned.
		EC412.5	Design and implement the appropriate machine learning techniques for different real-world problems.
S.No	Course Code & Title		Course Outcomes
		EC328.1	Illustrate how the speech production is modelled.
	5000000	EC328.2	Signal digitization and basic signal processing in time and frequency domains.
18	EC328 & Speech	EC328.3	Summarize the various techniques involved in collecting the features from the speech signal in both time and frequency domain.
	Recognition	EC328.4	Compare the various techniques involved in speech and speaker detection.
		EC328.5	Statistical framework used for automatic speech recognition
		,	
S.No	Course Code & Title		Course Outcomes
		EC322.1	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships.
10	EC-332 & Information	EC322.1	
19	EC-332 & Information Theory and Coding		Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships.
19		EC322.2	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems
19		EC322.2 EC322.3	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction
19		EC322.2 EC322.3 EC322.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection
19 S.No		EC322.2 EC322.3 EC322.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection
	Theory and Coding	EC322.2 EC322.3 EC322.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction
	Theory and Coding Course Code & Title	EC322.2 EC322.3 EC322.4 EC322.5	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes
	Theory and Coding Course Code & Title EC416 & Pattern	EC322.2 EC322.3 EC322.4 EC322.5	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques.
S.No	Theory and Coding Course Code & Title	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form.
S.No	Theory and Coding Course Code & Title EC416 & Pattern	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature.
S.No	Theory and Coding Course Code & Title EC416 & Pattern	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis.
S.No	Theory and Coding Course Code & Title EC416 & Pattern	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis.
5.No 20	Theory and Coding Course Code & Title EC416 & Pattern Recognition	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4 EC416.5	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern vertical and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers.
5.No 20	Course Code & Title EC416 & Pattern Recognition Course Code & Title	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4 EC416.5	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers. Course Outcomes
20 5.No	Course Code & Title EC416 & Pattern Recognition Course Code & Title	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4 EC416.5	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers. Course Outcomes Describe the basic architecturs like RISC and CISC
5.No 20	Course Code & Title EC416 & Pattern Recognition Course Code & Title EC 430 Advance Computer	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.5 EC416.5 EC430.1 EC430.2 EC430.2	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers. Course Outcomes Describe the basic architecturs like RISC and CISC Explain Pipelined and Multiprocessors systems architecture
20 5.No	Course Code & Title EC416 & Pattern Recognition Course Code & Title	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.5 EC416.5 EC430.1 EC430.1 EC430.3 EC430.4	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and written form. Apply pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers. Course Outcomes Describe the basic architecturs like RISC and CISC Explain Pipelined and Multiprocessors systems architecture Understand and analyse the memory systems and their implementations
20 5.No	Course Code & Title EC416 & Pattern Recognition Course Code & Title EC 430 Advance Computer	EC322.2 EC322.3 EC322.4 EC322.5 EC416.1 EC416.2 EC416.3 EC416.4 EC416.5 EC430.1 EC430.2 EC430.3 EC430.3 EC430.3	Understand and apply fundamental concepts in information theory such as probability, entropy, information content and their inter-relationships. Compute information theoretic quantities, construct bounds and implement algorithms for source coding and noisy channel theorems Apply linear block codes for error detection and correction Understand Binary Cyclic Codes, encoding with (n-k)-Stage Shift Register and Syndrome Calculations and Error Detection Apply convolution codes for performance analysis for error detection and correction Course Outcomes Understand fundamental concepts of Pattern classification, structural pattern recognition, and pattern classifier combination techniques. Summarize, assess, and relate research in the domain of pattern recognition in verbal and write forming the forming particular pattern recognition performance evaluation methodologies and evaluate comparisons of techniques in the academic literature. Use pattern recognition techniques to solve real-world challenges like document recognition and analysis. Implement simple pattern classifiers, classifier combinations, and structural pattern recognizers. Course Outcomes Describe the basic architecturs like RISC and CISC Explain Pipelined and Multiprocessors systems architecture Understand and analyse the memory systems and their implementations Designing different peripheral devices